
High Speed Cmos Circuits For Optical Receivers 1st Edition

high-speed cmos camera - mikrotron - multiple slope off triple slope activated mirotron mbh provides a full range of high-speed imaging solutions for challenging applications in industry, **noil2sm1300a - lupa1300-2: high speed cmos image sensor** - noil2sm1300a onsemi 3 specifications key specifications table 1. general specifications parameter specifications active pixels 1280 (h) x 1024 (v) **high speed cmos cameras - pco** - 6 pco.dimax hd | high speed cmos cameras applications the recording of high speed sequences of safety tests is a requirement by law for car manufacturers. **advanced high-speed cmos (ahc) logic family (rev. c)** - 2 advanced high-speed cmos (ahc) ahc can be used for higher-speed applications. some advantages of using ahc over hc are: •the ahc logic family is almost three times faster than the hc family. **video processing applications of high speed cmos image sensors** - advantage of integration is the ability to exploit the high speed imaging capability of cmos image sensors to enable new applications and to improve the performance of existing still and video processing applications. **high speed cmos image sensors - cypress** - this paper gives an overview of high speed cmos image sensor types and markets in which these can be used. the sensors the sensors presented in this paper are sensors designed for use in a general purpose, high end or custom high speed cameras. **lsttl logic ics v - texas instruments** - cd74hc4017-q1 high-speed cmos logic decade counter/divider with 10 decoded outputs scl546sa – october 2003 – revised april 2008 2 post office box 655303 • **design and simulation of a high speed cmos comparator** - design and simulation of a high speed cmos comparator 77 the double tail comparator offers a large current in the re-generative stage for fast re- **high-speed cmos logic hex inverter (rev. a)** - 4 quiescent device current icc vcc or gnd 0 6 - - 2 - 20 - 40 μ a hct types high level input voltage vih - - 4.5 to 5.5 2- - 2 - 2 - v low level input voltage **low cost cmos, high speed, rail-to-rail amplifiers** - ada4891-4 family provides high performance and versatility. the rail-to-rail output stage enables the output to swing to within 50 mv of each rail, enabling maximum dynamic range. **low-power, high-speed cmos analog switches** - vishay siliconix dg401b, dg403b, dg405b document number: 73069 s11-0179-rev. b, 07-feb-11 vishay 1 low-power, high-speed cmos analog switches **design of a high-speed cmos comparator** - keywords comparator, cmos comparator, sigma-delta adc, low power design, high-speed. abstract this master thesis describes the design of high-speed latched comparator with 6-bit resolution, full scale voltage **high speed cmos - engineering** - towards high speed and bandwidth efficient cmos wireless transceivers g. pottie, a. abidi, f. behbahani, e. berg, b. daneshrad, c. hansen, m. johnson,

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